



Finite State Machines in Hardware: Theory and Design (with VHDL and SystemVerilog) (Hardback)

By Volnei A. Pedroni

MIT Press Ltd, United States, 2014. Hardback. Book Condition: New. 229 x 183 mm. Language: English . Brand New Book. Modern, complex digital systems invariably include hardware-implemented finite state machines. The correct design of such parts is crucial for attaining proper system performance. This book offers detailed, comprehensive coverage of the theory and design for any category of hardware-implemented finite state machines. It describes crucial design problems that lead to incorrect or far from optimal implementation and provides examples of finite state machines developed in both VHDL and SystemVerilog (the successor of Verilog) hardware description languages. Important features include: extensive review of design practices for sequential digital circuits; a new division of all state machines into three hardware-based categories, encompassing all possible situations, with numerous practical examples provided in all three categories; the presentation of complete designs, with detailed VHDL and SystemVerilog codes, comments, and simulation results, all tested in FPGA devices; and exercise examples, all of which can be synthesized, simulated, and physically implemented in FPGA boards. Additional material is available on the book's Website. Designing a state machine in hardware is more complex than designing it in software. Although interest in hardware for finite state machines has...

DOWNLOAD



 **READ ONLINE**

Reviews

If you need to adding benefit, a must buy book. I could comprehended every thing out of this composed e pdf. I am just very happy to tell you that this is the greatest pdf i have study inside my individual existence and could be the finest publication for at any time.

-- **Miss Laurie Waters IV**

Most of these publication is the greatest publication offered. It is actually rally intriguing through reading period of time. You can expect to like just how the article writer create this publication.

-- **Eddie Schuppe**