Cadence Allegro SPB 16.3 succinctly and frequently used functions and application examples [paperback]

By HE YONG

paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Paperback. Pages Number: 491 Language: Simplified Chinese. Publisher: Electronic Industry Press; 1st edition (February 1. 2011). Cadence Allegro SPB is a very wide application of high-speed circuit design platform . Cadence Allegro SPB 16.3 Jingjiang common functions and application examples for the latest version of the 16.3 series. an example succinctly described in detail in the form of the common features of the platform. application design methods and techniques. The book is divided into four 18. Chapter 1 for the Allegro SPB schematic design. describes the Allegro SPB features. system configuration and installation. Design Entry CIS schematic design platform. produce schematic component packaging. and design specifications and schematic its instances; the first two for the PCB layout design. the system introduces the PCB Editor Layout design environment. production and packaging of PCB component instance. the establishment of circuit board. and the constraints set Constraint Manager. PCB layout techniques and examples. shop copper technology and its instance. PCB layout techniques and examples. PCB post-processing. design output toFour Satisfaction guaranteed,or money back.

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